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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete If Known	
Application Number	10/033,102
Filing Date	10/25/2001
First Named Inventor	Robert D. Gardner
Art Unit	2195
Examiner Name	Syed J. Ali
Attorney Docket Number	10011537-1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		Hewlett-Packard Product Manual, "Overview of IA-64 Architecture	
		Hewlett-Packard Product Manual, "IA-64 Software Conventions and Runtime Architecture", Version 1.0, August 31, 1999.	
		Intel Product Manual, "Intel IA-64 Architecture Software Developer's Manual", Volume 1: IA-64 Application Architecture, Revision 1:1, July 2000.	
		Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 2: IA-64 System Architecture, Revision 1.1, July 2000.	
		Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 3: IA-64 System Architecture, Revision 1.1, July 2000.	
		Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 4: IA-64 System Architecture, Revision 1.1, July 2000.	
		Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Itanium Processor Microarchitecture Reference for Software Optimization, Aug. 2000.	

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